

To: Examiner H. Gibbs

From: Ilka Dalton for Michael J. Will

6-12-06 6:39pm p. 1 of 20

Please find attached for filing in connection with application no. 10/034,860, entitled CIRCUIT OF SCANNER TO PERFORM COLOR SPACE CONVERSION ON RGB SIGNAL, the following documents:

- (1) Petition to Withdraw Holding of Abandonment Under 37 CFR 1.181 (3 pages)
- (2) Copy of Transaction History of Serial No. 10/034,860 from Public PAIR (1 page)
- (3) Return Receipt Postcard bearing a stamp by OIPE dated 11/28/05 (1 page)
- (4) Fee Transmittal form PTO/SB-17 bearing OIPE stamp dated 11/28/05 (1 page)
- (5) Office Action Response with cover sheet bearing stamp by OIPE dated 11/28/05 (9 pages)
- (6) Transmittal Form PTO/SB/21 bearing stamp by OIPE dated 11/28/05 and Certificate of Transmission signed by Jessica Harvey dated 11/23/05 (1 page)
- (7) Form PTO/SB/96 bearing stamp by OIPE dated 11/28/05 (1 page)
- (8) Power of Attorney Form PTO/SB/80 bearing stamp by OIPE dated 11/28/05 (1 page)

Ilka Dalton

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To:	Gibbs, H.	From:	Michael J. Willardson
Fax:	571.273.8300	Pages:	19 pages, including this cover
Phone:		Date:	June 12, 2006
Our Ref:	112.P14194	CC:	

☐ **Urgent** ☐ **For Review** ☐ **Please Comment** ☐ **Please Reply** ☐ **Please Recycle**

Please find attached for filing in connection with application no. 10/034,860, entitled **CIRCUIT OF SCANNER TO PERFORM COLOR SPACE CONVERSION ON RGB SIGNAL**, the following documents:

- (1) Petition to Withdraw Holding of Abandonment Under 37 CFR 1.181 (3 pages)
- (2) Copy of Transaction History of Serial No. 10/034,860 from Public PAIR (1 page)
- (3) Return Receipt Postcard bearing a stamp by OIPE dated 11/28/05 (1 page)
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June 12, 2006

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112.P14194

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Chen et al.

Application No.: 10/034,860

Filed: December 27th, 2001For: Circuit of Scanner to Perform Color Space
Conversion on RGB SignalExaminer: *Gibbs, H.*

Art Unit: 2625

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PETITION TO WITHDRAW HOLDING OF ABANDONMENT UNDER 37 CFR §1.181COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Abandonment, dated April 10, 2006, Assignee respectfully petitions the Office under 37 CFR §1.181 to withdraw the holding of abandonment.

ARGUMENT

In an Office Communication dated April 10, 2006, the Examiner issued a notice of abandonment for the above-identified patent application. In this notice, the Examiner indicated that the application was abandoned for failure to timely file a proper reply to the Office Action mailed on August 25, 2005.

However, it is respectfully submitted that a fully responsive reply to the August 25, 2005 office action was timely filed with the USPTO within the statutory period for reply. In addition, a properly executed Power of Attorney and a change of correspondence address were submitted along with the timely filed Office Action Response. The attached printout from Public PAIR clearly indicates that the response to office action and accompanying documents were received by the USPTO on November

112.P14194

28, 2005. In addition, the following attached documents provide further evidence of timely mailing and receipt of the response to office action:

1. Return receipt postcard, bearing a stamp by OIPE dated November 28, 2005.
2. Fee transmittal form PTO/SB/17, bearing a stamp by OIPE dated November 28, 2005.
3. Office action response with cover sheet bearing a stamp by OIPE dated November 28, 2005.
4. Transmittal form PTO/SB/21, bearing a stamp by OIPE dated November 28, 2005 and certificate of transmission/mailing signed by Jessica A. Harvey dated November 23, 2005.
5. Form PTO/SB/96, bearing a stamp by OIPE dated November 28, 2005.
6. Power of Attorney, form PTO/SB/80, bearing a stamp by OIPE dated November 28, 2005.

It is respectfully submitted that the attached documents provide adequate evidence to support the finding that the office action response mailed on November 23, 2005 was timely filed. According to 37 CFR §1.8:

- (1) Correspondence will be considered as being timely filed if:
 - (i) The correspondence is mailed or transmitted prior to expiration of the set period of time by being:
 - (A) Addressed as set out in § 1.1(a) and deposited with the U.S. Postal Service with sufficient postage as first class mail; or
 - (B) Transmitted by facsimile to the Patent and Trademark Office in accordance with § 1.6(d); and
 - (ii) The correspondence includes a certificate for each piece of correspondence stating the date of deposit or transmission. The person signing the certificate should have reasonable basis to expect that the correspondence would be mailed or transmitted on or before the date indicated.

Therefore, it is respectfully submitted that in accordance with 37 CFR §1.8, and based on the evidence presented herein, the response to office action was timely filed, and the notice of abandonment was improperly issued. It is, therefore, respectfully requested that the holding of abandonment be withdrawn, and the response to office action fully considered for the above-referenced patent application.

112.P14194


CONCLUSION

In view of the foregoing, it is respectfully requested that the holding of abandonment of this patent application be withdrawn and that the fully responsive Response to Office action mailed on November 23, 2005 be entered in this patent application. If the Examiner has any questions, she is invited to contact the undersigned at (503) 439-6500.

Respectfully submitted,

Dated: _____

6/12/06



Michael J. Willardson
Patent Attorney
Reg. No. 50,856

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Customer No. 43831

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10/034,860 Circuit of scanner to perform color space conversion on RGB signal

Application Data	Transaction History	Image File Wrapper	Foreign Priority	Published Documents	Publication Dates	Address & Attorney/Agent
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Transaction History

Date	Transaction Description
11-28-2005	Preliminary Amendment
04-10-2006	Mail Abandonment for Failure to Respond to Office Action
04-03-2006	Abandonment for Failure to Respond to Office Action
03-21-2006	Case Docketed to Examiner in GAU
08-25-2005	Mail Non-Final Rejection
08-22-2005	Non-Final Rejection
06-28-2005	Case Docketed to Examiner in GAU
03-18-2005	IFW TSS Processing by Tech Center Complete
01-31-2004	Case Docketed to Examiner in GAU
02-12-2002	Application Dispatched from OIPE
02-08-2002	Application Is Now Complete
01-16-2002	IFW Scan & PACR Auto Security Review
12-27-2001	Initial Exam Team nn

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- Call the Patent Electronic Business Center at (866) 217-9197 (toll free) or e-mail EBC@uspto.gov for specific questions about Patent Application Information Retrieval (PAIR).
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- If you experience technical difficulties or problems with this application, please reach out via e-mail to [Electronic Business Support](#) or call 1 800-786-9199.

The stamp of the PTO hereon acknowledges receipt of the following documents in re patent application for:

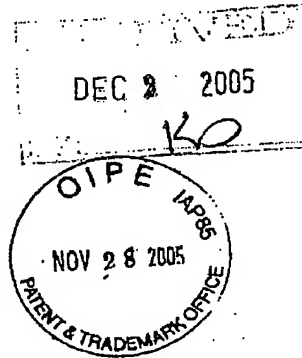
**CIRCUIT OF SCANNER TO PERFORM COLOR SPACE
CONVERSION ON RGB SIGNAL**

- ☒ Transmittal (1 page)
- ☒ Fee Transmittal (1 page)
- ☒ Amendment (9 pages)
- ☒ Statement Under 37 CFR 3.73(b) (1 page)
- ☒ Power of Attorney (1 page)
- ☒ Assignment of Patent Rights (75 pages)
- ☒ Return Receipt Postcard

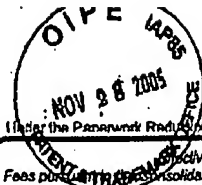
Filing Date: December 27, 2001
Serial No.: 10/034,860

Mailing Date: November 23, 2005
BLTG Docket No. 112.P14194

MJW:jah



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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

This form is effective on 12/08/2004.

Fees paid with this form are consolidated Appropriations Act, 2005 (H.R. 4918).

FEE TRANSMITTAL

For FY 2005

☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$) 200.00

PTO/SB/17 (12-04v2)

Approved for use through 07/31/2008, OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Complete if Known

Application Number 10/034,860
Filing Date 12/27/2001
First Named Inventor Chun-Jen Chen
Examiner Name H. Gibbs
Art Unit 2622
Attorney Docket No. 112.P14184

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FEE CALCULATION

1. BASIC FILING, SEARCH, AND EXAMINATION FEES

Application Type	FILING FEES		SEARCH FEES		EXAMINATION FEES		Fees Paid (\$)
	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	

2. EXCESS CLAIM FEES

Fee Description	Fee (\$)	Small Entity Fee (\$)
Each claim over 20 (including Reissues)	50	25
Each independent claim over 3 (including Reissues)	200	100
Multiple dependent claims	360	180
Total Claims		
Extra Claims		
Fee (\$)		
Fee Paid (\$)		
Multiple Dependent Claims		
Fee (\$)		
Fee Paid (\$)		

HP = highest number of total claims paid for, if greater than 20.

Inden. Claims Extra Claims Fee (\$)

4 - 3 or HP = 1 x 200 = 200.00

HP = highest number of independent claims paid for, if greater than 3.

3. APPLICATION SIZE FEE

If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).

Total Sheets Extra Sheets Number of each additional 50 or fraction thereof Fee (\$)

- 100 = / 50 = (round up to a whole number) x

4. OTHER FEE(S)

Non-English Specification, \$130 fee (no small entity discount)

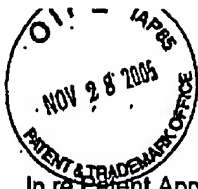
Other (e.g., late filing surcharge):

SUBMITTED BY		Registration No.	Telephone
Signature		(Attorney/Agent) 50,866	603.439.8500
Name (Print/Type)	Michael J. Willardson	Date	11/23/05

This collection of information is required by 37 CFR 1.138. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Attorney Docket: 112.P14194



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Chen et al.

Application No.: 10/034,860

Filed: December 27th, 2001

For: Circuit of Scanner to Perform Color Space
Conversion on RGB Signal

Examiner: Gibbs, H.

Art Unit: 2622

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AMENDMENT

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Dear Sir:

In response to the Office Action, dated August 25th, 2005, please enter the following
amendments and consider the following remarks.

Amendments begin on page 2.

Remarks begin on page 7.

11/29/2005 HBAUTEM1 00000006 503703 10034860
01 FC:1201 200.00 DA

AMENDMENT**IN THE CLAIMS:**

This listing of claims will replace all prior versions and listings of claims in the application:

1. (Currently Amended) ~~A circuit of a scanner to perform color space conversion for a RGB signal, comprising:~~

~~a plurality of sampling-amplified-offset devices, gain amplifiers, adapted to sample, amplify and compensate levels of an R-charge signal, a G-charge signal and a B-charge signal, respectively, to obtain an R-analog signal, a G-analog signal and a B signal impart a gain to an associated plurality of color signals according to a first color model;~~

~~an adder, to perform an addition calculation on the R-analog signal, the G-analog signal and the B-analog signal to obtain an addition analog signal combine said amplified color signals to provide one or more output signals; and~~

~~a multiplexer, to select the R-analog signal, the G-analog signal, the B-analog signal or the addition analog signal as output a circuit to set gains associated with said gain amplifiers to provide said one or more output signals as one or more color signals of a second color model.~~

2. (Currently Amended) ~~The circuit according to claim 1, wherein each of the sampling-amplified-offset devices further comprises:~~

~~a correlation double sampler, to perform sampling two times on the R, G or B-charge signal, and to perform a subtraction operation on results of the two samplings to obtain a luminance;~~

~~a programmable gain amplifier, to adjust a gain value to amplify the luminance, and to obtain an amplified luminance according to the gain value; and~~

~~an offset device, to compensate level of the amplified luminance to obtain the R, G or B-analog signal of the R, G or B-charge signals, respectively said first color model comprises an RGB color model.~~

3. (Currently Amended) The circuit according to claim 1, wherein each of the sampling-amplified-offset devices further comprises:

a correlation double sampler, to perform sampling two times on the R, G or B charge signal, and to perform a subtraction operation on results of the two samplings to obtain a luminance;
an offset device, to compensate a level of the luminance to obtain a compensated luminance;
and
a programmable gain amplifier, to adjust a gain value to amplify the compensated luminance, and to obtain the R, G or B analog signal of the R, G or B charge signals wherein said second color model comprises a YUV color model.

4. (Cancelled)

5. (Currently Amended) A circuit of a scanner to perform a color space conversion on an RGB signal, comprising:

a plurality of sampling-amplified-offset devices, to sample, amplify and and/or compensate levels of an a R charge signal, a G charge signal and a B charge signal, respectively, to obtain an a R analog signal, a G analog signal and a B analog signal;

a gain adder, adapted to multiply each one or more of the R, G and B analog signals by a corresponding weighted value, and to add one or more of the R, G and B analog signals multiplied by the weighted values to obtain an addition a summed analog signal; and

a multiplexer, to select one or more of the R analog signal, the G analog signal, the B analog signal or the addition summed analog signal as an output signal.

6. (Currently Amended) The circuit according to claim 5, wherein one or more each of the sampling-amplified-offset devices further comprises:

a correlation double sampler, to perform sampling twice obtain a plurality of samples on the one or more of the R, G or B charge signal and to perform a subtraction operation on results of the two

~~sample to obtain~~ determine a luminance based at least in part on a difference between at least two
of said samples;

a programmable gain amplifier, ~~to adjust a gain value to amplify the luminance and to obtain an~~
amplified luminance according to the g gain value; and

an offset device, to compensate ~~a level of the amplified luminance to obtain the R, G or B~~
analog signal of the one or more of the R, G and B charge signals, respectively, based at least in part
on the obtained amplified luminance.

7. (Currently Amended) The circuit according to claim 5, wherein one or more each of the sampling-
amplified-offset devices further comprises:

a correlation double sampler, to ~~perform sampling twice~~ obtain a plurality of samples of one or
more of ~~en~~ the R, G or B charge signals and to ~~perform a subtraction operation on results of the two~~
~~sampling samplings to obtain~~ obtain a luminance;

an offset device, to compensate a level of the luminance to obtain a compensated luminance;
and

a programmable gain amplifier, to adjust a gain value to amplify the compensated luminance
and to obtain one or more of the R, G or B analog signals of the one or more R, G or B charge signal,
respectively.

8. (Currently Amended) The circuit according to claim 5, wherein the gain adder further includes:

a plurality of gain amplifiers, to multiply the one or more of the R analog signal, the G analog
signal, the B analog signal by the corresponding weighted gains to obtain a plurality of weighted analog
signals; and

an adder, to add the weighted analog signals to obtain the addition analog signal.

9. (Currently Amended) The circuit according to claim 5, wherein the multiplexer selects one or more of
the R, G or B analog signals and outputs a selected one to an analog-digital converter, ~~which then~~
~~converts the selected one into a digital signal.~~

10. (Currently Amended) ~~A circuit of a scanner to perform a color space conversion on an RGB signal,~~
comprising:

a plurality of sampling-amplified-offset devices, to sample, amplify and compensate levels of ~~a~~ an R charge signal, a G charge signal and a B charge signal, respectively, to obtain ~~an~~ a R analog signal, a G analog signal and a B analog signal;

a plurality of gain adders, adapted to multiply each of the R, G and B analog signals by different weighted values to obtain a plurality of results, and to add the results of each of the R, G and B analog signals into a plurality of addition analog signals; and

a multiplexer, adapted to select the R analog signal, the G analog signal, the B analog ~~signal~~ or the addition analog signals as output.

11. (Currently Amended) The circuit according to claim 10, wherein one or more each of the sampling-amplified-offset devices further comprises:

a correlation double sampler, adapted to perform sampling twice on the R, G or B charge signals, and to perform a subtraction operation on results of the two samplings to obtain a luminance;

a programmable gain amplifier, adapted to adjust a gain value to amplify the luminance and to obtain an amplified luminance according to the gain value; and

an offset device, adapted to compensate level of the amplified luminance to obtain the R, G or B analog signal of the R, G and B charge signal, respectively.

12. (Currently Amended) The circuit according to claim 10, wherein one or more each of the sampling-amplified-offset devices further comprises:

a correlation double sampler, adapted to perform sampling twice on the R, G or B charge signal and to perform a subtraction operation on results of the two samplings to obtain a luminance;

an offset device, adapted to compensate a level of the luminance to obtain a compensated luminance; and

a programmable gain amplifier, adapted to adjust a gain value to amplify the compensated luminance, and to obtain the R, G or B analog signal of the R, G and B charge signal, respectively.

13. (Currently Amended) The circuit according to claim 10, wherein one or more each of the gain adders further includes:

a plurality of gain amplifiers, adapted to multiply the R analog signal, the G analog signal, the B analog signal by the corresponding weighted gains to obtain a plurality of weighted analog signals; and
an adder, adapted to add the weighted analog signals to obtain the addition analog signal.

14. (Currently Amended) The circuit according to claim 10, wherein the multiplexer selects the R, G or B analog signals and outputs a selected one to an analog-digital converter, ~~which then converts the selected one into~~ to form a digital signal.

15. (New) A method, comprising:

obtaining one or more color signals according to a first color model;
amplifying one or more of the color signals to obtain one or more amplified signals;
adding at least a portion of the amplified signals to obtain one or more output signals; and
providing said one or more output signals as one or more color signals according to a second color model.

16. (New) The method of claim 15, wherein the first color model comprises an RGB color model.

17. (New) The method of claim 15, wherein the first color model comprises an YUV color model.

Attorney Docket 112.174194

REMARKS

Claims 1-3 and 5-17 are pending in the above-referenced patent application. Claims 1-3 and 5-14 have been amended, claim 4 has been cancelled and claims 15-17 have been added. It is noted that these claims were not amended to overcome the rejections and objections, or in light of the cited documents. Rather, claims 1-14 were amended to more clearly delineate intended subject matter. Furthermore, it is believed that these amendments do not narrow claim scope. Rather, in some cases, the claims are even broadened. Therefore, no prosecution history estoppel should result from these claim amendments. Additionally, it is respectfully submitted that adequate support may be found in the specification for new claims 15-17. For example, adequate support may be found in page 5 line 20 to page 6 line 12, page 7 lines 14-25, Fig. 1 and Fig. 2.

In the Office Action, dated August 25th, 2005, the Examiner made various objections to claims 1, 5 and 10 for various informalities. Assignee has amended the claims to obviate the objections. Additionally, the Examiner rejected claims 1-8, 10-12 and 14 under 35 U.S.C 103(a) as being unpatentable over "Applicant's Admitted Prior Art" (APA) in view of Cheng (US Patent No. 6,052,147). This rejection is respectfully traversed. It is noted that claims 1, 5 and 10 have been amended in accordance with Examiners request.

Assignee respectfully submits that claims 1-3, 5-8, 10-12 and 14, as amended, are not rendered obvious by APA in view of Cheng. Although Assignee does not agree with the Examiner's characterization of what Applicant has admitted as prior art, it is respectfully submitted that APA, whether viewed alone or in combination with Cheng, is still lacking at least one element of the rejected claims.

As just an example, referring to claim 1, as amended, APA does not show or describe at least "an adder, to combine said amplified color signals to provide one or more output signals, and a circuit to set gains associated with said gain amplifiers to provide said one or more output signals as one or more color signals of a second color model." Additionally, Cheng does not cure this deficiency. Cheng describes an adder that may be implemented in a voltage regulating circuit and "can be included before amplified via the signal amplifier 123" (col 5:33 - col 5:34), and does not show or describe at least the

Attorney Docket 712.P14184

limitations noted above. For example, the adder described in Cheng performs adding functions prior to any amplifying in the voltage regulating circuit, and is materially different from what is claimed and described with respect to claim 1, as amended. See, for example, Fig. 1 of Cheng.

Assignee respectfully submits that, for at least the reasons presented above, a prima facie case of obviousness has not been established, and, therefore, claim 1, as amended, is in a condition for allowance. It is noted that many other bases for traversing the rejection could be provided, but Assignee believes that this ground is sufficient. Additionally, claims 2-3 and 5-14, as amended, are in a condition for allowance for the same and/or similar reasons as presented with reference to claim 1, as amended, and new claims 15-17 are in a condition for allowance for the same and/or similar reasons as presented with reference to claim 1, as amended. It is respectfully requested that the Examiner withdraw his rejections of these claims.

CONCLUSION

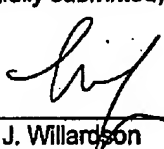
In view of the foregoing, it is respectfully submitted that all of the claims pending in this patent application, as amended, are in condition for allowance. If the Examiner has any questions, she is invited to contact the undersigned at (503) 439-6500. Reconsideration of this patent application and early allowance of all the claims is respectfully requested.

Please charge any shortages and credit any overcharges of any fees required for this submission to Deposit Account number 50-3703.

Respectfully submitted,

Dated: _____

11/23/05


Michael J. Willardson
Patent Attorney
Reg. No. 50,856

Berkeley Law and Technology Group, LLC
1700 NW 167th Place, Suite 240
Beaverton, OR 97006



PTO/SB/21 (08-03)
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/034,860	
	Filing Date	12/27/2001	
	First Named Inventor	Chun-Jen Chen	
	Art Unit	2822	
	Examiner Name	H. Gibbs	
Total Number of Pages in This Submission	88	Attorney Docket Number	112.P14184

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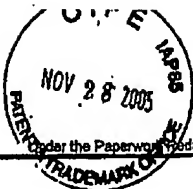
JUN 12 2006

ENCLOSURES (Check all that apply)		
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Remarks		
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual name	Michael J. Willardson, Reg. No. 60,856 Berkeley Law and Technology Group, LLC	
Signature		
Date	11/28/05	

CERTIFICATE OF TRANSMISSION/MAILING		
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.		
Typed or printed name	Jessica A. Harvey	
Signature		Date 11/23/05

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/98 (08-03)

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Transpacific IP, Ltd.Application No./Patent No.: 10/034,860 Filed/Issue Date: 12/27/2001Entitled: CIRCUIT OF SCANNER TO PERFORM COLOR SPACE CONVERSION ON RGB SIGNALTranspacific IP, Ltd.

(Name of Assignee)

a Company

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or2. ☐ an assignee of less than the entire right, title and interest.

The extent (by percentage) of its ownership interest is _____ %

In the patent application/patent identified above by virtue of either:

A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

OR

B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:1. From: Chun-Jen Chen et al. To: Umax Data Systems, Inc.The document was recorded in the United States Patent and Trademark Office at Reel 012436, Frame 0407, or for which a copy thereof is attached.2. From: Umax Data Systems, Inc. To: Veutron CorporationThe document was recorded in the United States Patent and Trademark Office at Reel 016800, Frame 0203, or for which a copy thereof is attached.3. From: Veutron Corporation To: Transpacific IP, Ltd.

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☐ Additional documents in the chain of title are listed on a supplemental sheet.☒ Copies of assignments or other documents in the chain of title are attached.

(NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08)

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Date

503.439.6500

Telephone number

Michael J. Willardson/Reg. No. 50,856

Typed or printed name

Signature

Patent Attorney

Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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POWER OF ATTORNEY TO PROSECUTE APPLICATIONS BEFORE THE USPTO

I hereby revoke all previous powers of attorney given in the application identified in the attached statement under 37 CFR 3.73(b).

I hereby appoint:

☒ Practitioners associated with the Customer Number:

00043831

OR

☐ Practitioner(s) named below (If more than ten patent practitioners are to be named, then a customer number must be used):

Name	Registration Number	Name	Registration Number

an attorney(s) or agent(s) to represent the undersigned before the United States Patent and Trademark Office (USPTO) in connection with any and all patent applications assigned only to the undersigned according to the USPTO assignment records or assignment documents attached to this form in accordance with 37 CFR 3.73(b).

Please change the correspondence address for the application identified in the attached statement under 37 CFR 3.73(c) to:

☒ The address associated with Customer Number:

00043831

OR

☐ Firm or Individual Name

Address

City

State

Zip

Country

Telephone

Email

Appointee Name and Address:

Transpacite IP, Ltd.
14FL, No. 188
Tun Hwa N. Road
Taipei, Taiwan, ROC

A copy of this form, together with a statement under 37 CFR 3.73(b) (Form PTO/SB/60 or equivalent) is required to be filed in each application in which this form is used. The statement under 37 CFR 3.73(b) may be completed by one of the practitioners appointed in this form if the appointed practitioner is authorized to act on behalf of the assignee, and must identify the application in which this Power of Attorney is to be filed.

SIGNATURE of Assignee of Record

The individual whose signature and title is supplied below is authorized to act on behalf of the assignee

Signature	<i>Gay Prohm</i>	Date	25 AUGUST 2005
Name	Gay Prohm	Telephone	(415) 462-0515
Title	Authorized Person, Transpacite IP, Ltd.		

This collection of information is required by 37 CFR 1.51, 1.52 and 1.53. The information is required to obtain or retain a benefit by the public which is to be used by the USPTO to process an application. Confidentiality is provided by 39 U.S.C. 402 and 37 CFR 1.11 and 1.14. This collection is estimated to take 9 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1480, Alexandria, VA 22303-1480. DO NOT SEND PRELIMINARY OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1480, Alexandria, VA 22303-1480.

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